

## LISTING OF CLAIMS

1. (Currently Amended) A method comprising:

**executing a target program via a central processing unit (CPU), the target program to include a plurality of branches and to be executed in a virtual machine;**

configuring ~~the a central processing unit (CPU[[I]])~~ to notify an information collection module (ICM) **via an interrupt** when **[[a]] one of the plurality of branches in the target program** is taken, **wherein the ICM is part of a virtual machine monitor that is coupled to the virtual machine to collect code coverage information about the target program;**

recording a branch address when the ICM is notified **one** of **[[a]] the plurality of branches is** taken; **[[and]]**

storing the recorded branch addresses **in memory;** **[[to]]**

~~determine~~ **determining** code coverage of **the** target program **based on the branch addresses stored in memory.**

2. (Cancelled).

3. (Original) The method of claim 1, further comprising providing the recorded branch addresses to a coverage pattern generation module (CPGM) to interpret and display code coverage statistics.

- 4-10. (Cancelled).

11. (Currently Amended) An article of manufacture comprising[[(:)] a machine accessible medium including content that when accessed by a machine causes the machine to perform operations ~~comprising~~ **including**:

**executing a target program via a central processing unit (CPU), the target program to include a plurality of branches and to be executed in a virtual machine;**

configuring ~~the a central processing unit (CPU([:]))~~ to notify an information collection module (ICM) **via an interrupt** when [[a]] **one of the plurality of branches in the target program** is taken, **wherein the ICM is part of a virtual machine monitor that is coupled to the virtual machine to collect code coverage information about the target program;**

recording a branch address when the ICM is notified **one** of [[a]] **the plurality of branches** ~~is~~ taken; [[and]]

storing the recorded branch addresses **in memory**; [[to]]

determine **determining** code coverage of ~~the~~ target program **based on the branch addresses stored in memory.**

12. (Original) The article of manufacture of claim 11, wherein the machine accessible medium further includes content that causes the machine to perform operations comprising providing the recorded branch addresses to a coverage pattern generation module (CPGM) to interpret and display code coverage statistics.

13-20. (Cancelled).

21. (New) The article of manufacture of claim 12, wherein the CPGM comprises a graphical user interface (GUI) to accept a source file of the target program and the recorded coverage statistics.

22. (New) The method of claim 3, wherein the CPGM comprises a graphical user interface (GUI) to accept a source file of the target program and the recorded coverage statistics.

23. (New) A system comprising:

a central processing unit (CPU) to execute a target program to be executed in a virtual machine and including a plurality of branches; and

a virtual machine monitor, coupled to the virtual machine, to collect coverage information about the target program, the virtual machine monitor to include an information collection module (ICM) to

configure the CPU to notify the ICM when one of the plurality of branches in the target program is taken,

record a branch address when the ICM is notified one of the plurality of branches is taken,

store the recorded branch addresses in memory, and

determine code coverage of the target program based on the branch addresses stored in memory.

24. (New) The system of claim 23, further comprising a coverage pattern generation module (CPGM) to interpret and display code coverage statistics.

25. (New) The system of claim 24, wherein the CPGM comprises a graphical user interface (GUI) to accept a source file of the target program and the recorded coverage statistics.